

FIG.1

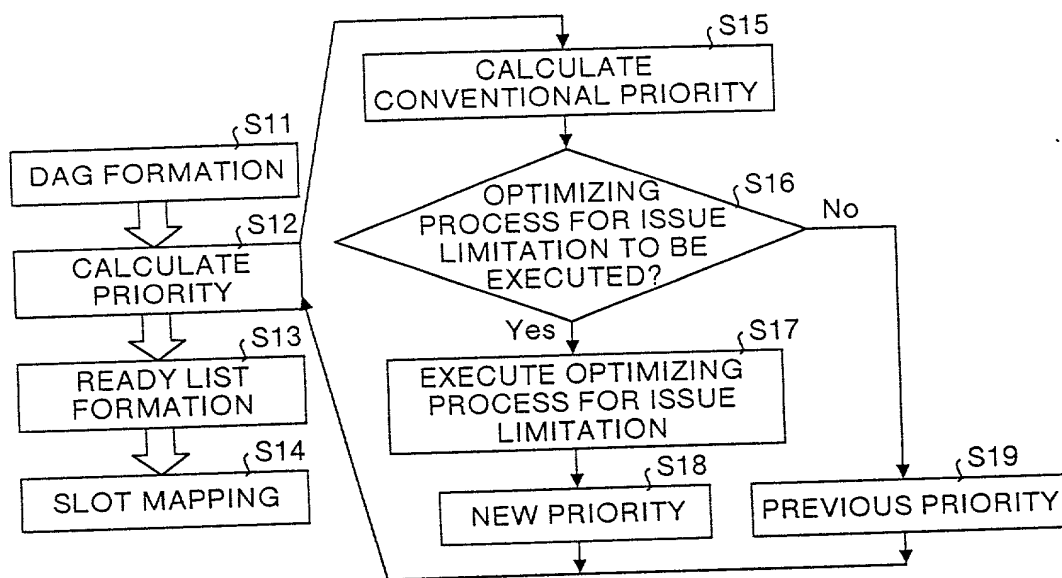


FIG.2

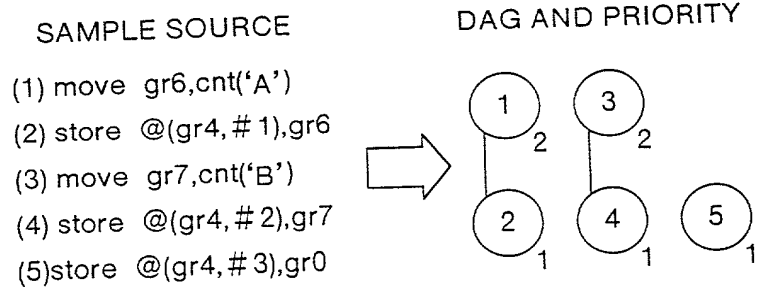


FIG.3

PRIORITY	COMMAND
2	(1)(3)
1	(2)(4)(5)

FIG.4

PRIORITY	NUMBER OF ACTUAL VLIW COMMANDS	NUMBER OF MINIMUM VLIW COMMANDS	PROCESS FOR ISSUE LIMITATION
2	1	1	NONE
1	3	2	PRESENT

FIG.5

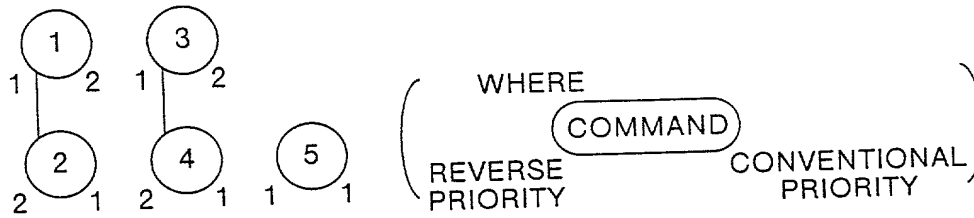


FIG.6

COMMAND	REVERSE PRIORITY VALUE	ORDER OF GENERATION	WEIGHT
(2)	2	1	1
(4)	2	2	0
(5)	1	3	2

FIG.7

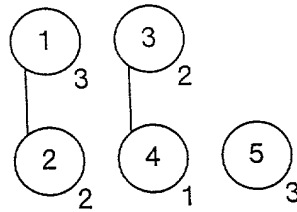


FIG.8

(5)→(1)→(2)→(3)→(4)
SLOT MAPPING IS
CARRIED OUT IN
SUCCESSION



PSEUDO MACHINE TABLE

	I0 SLOT	I1 SLOT
α	5	1
$\alpha+1$	2	3
$\alpha+2$	4	

FIG.9

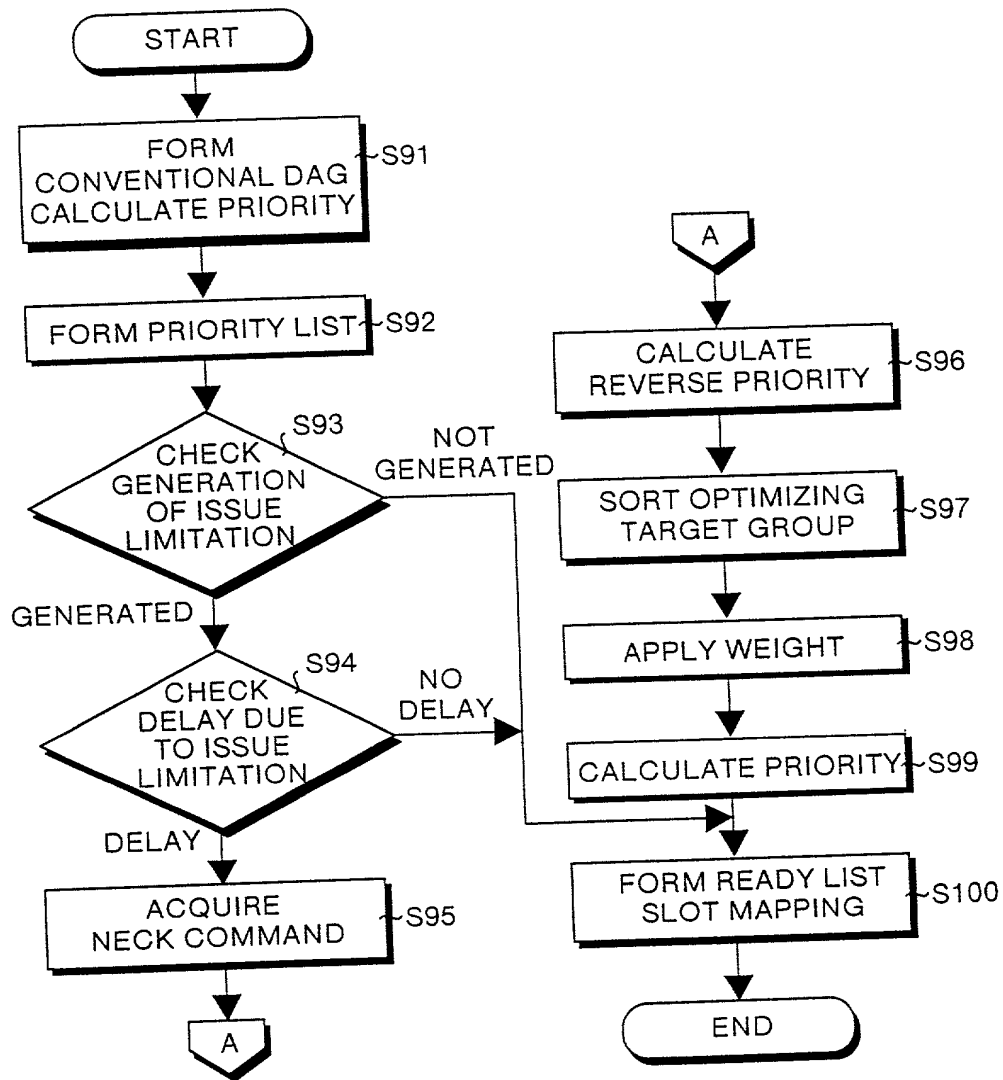


FIG.10

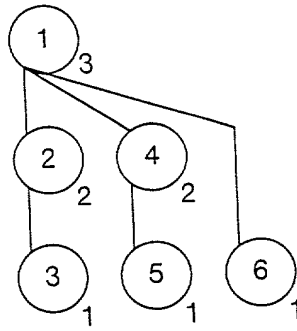


FIG.11

PRIORITY	COMMAND LIST
1	(3),(5),(6)
2	(2),(4)
3	(1)

FIG.12

PRIORITY	COMMAND LIST	RESULTS OF ISSUE LIMITATION CHECK
1	(3),(5),(6)	PRESENT
2	(2),(4)	NONE
3	(1)	NONE

FIG.13

NUMBER OF MINIMUM VLIW COMMANDS: 2			NUMBER OF ACTUAL VLIW COMMANDS: 3		
(NUMBER OF VLIW COMMANDS WITHOUT ISSUE LIMITATION)			(NUMBER OF VLIW COMMANDS WITH ISSUE LIMITATION)		
	I0 SLOT	I1 SLOT		I0 SLOT	I1 SLOT
VLIW1	3	5	<	3	
VLIW2	6			5	
				6	

FIG.14

NUMBER OF VLIW COMMANDS WITHOUT ISSUE LIMITATION (NUMBER OF MINIMUM VLIW COMMANDS)	NUMBER OF VLIW COMMANDS REQUIRED FOR ACTUAL ISSUE (NUMBER OF ACTUAL VLIW COMMANDS)
TWO	THREE

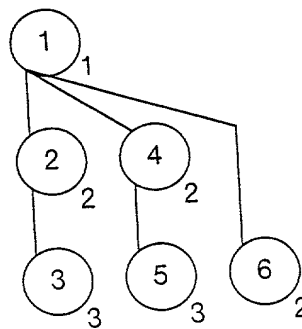
FIG.15

PRIORITY	COMMAND LIST	RESULTS OF ISSUE LIMITATION CHECK	DELAY CHECK
1	(3),(5),(6)	PRESENT	PRESENT
2	(2),(4)	NONE	—
3	(1)	NONE	—

FIG.16

NECK COMMAND	NECK PRIORITY
(1)	3

FIG.17



ADVANTAGE

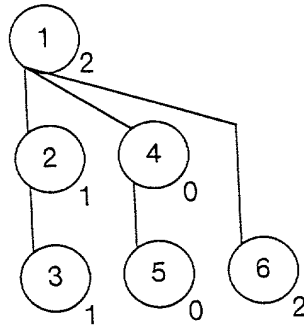


FIG.19

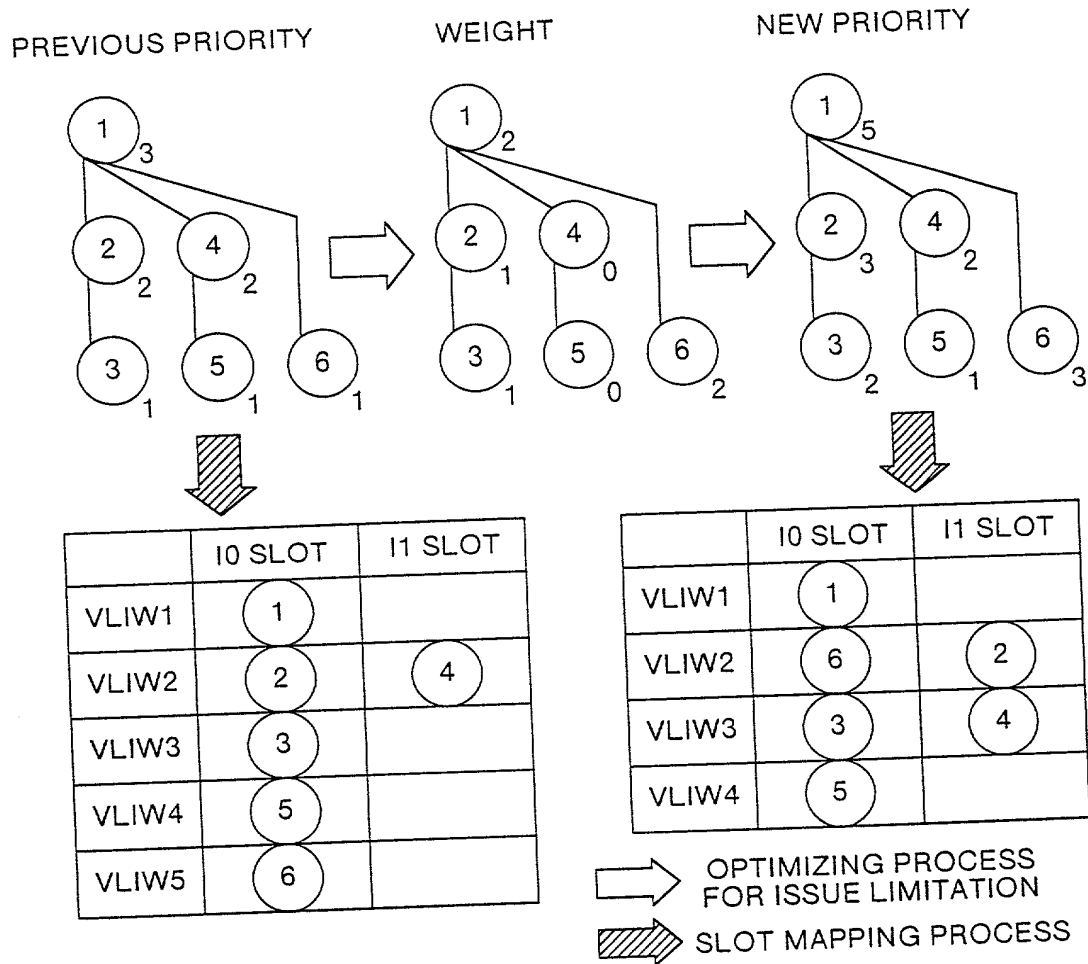


FIG.20

REVERSE PRIORITY

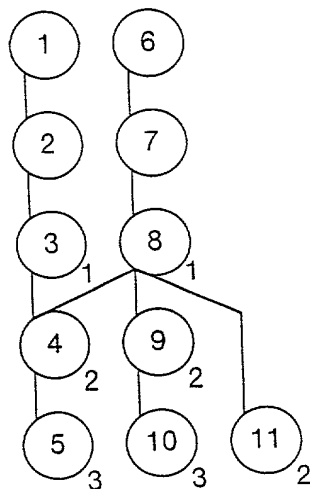


FIG.21

WEIGHT

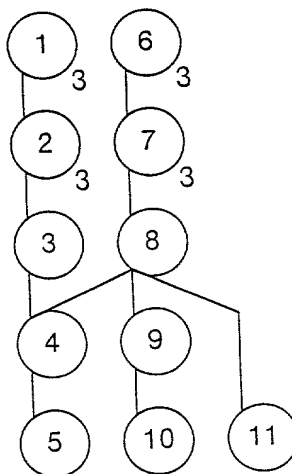


FIG.22

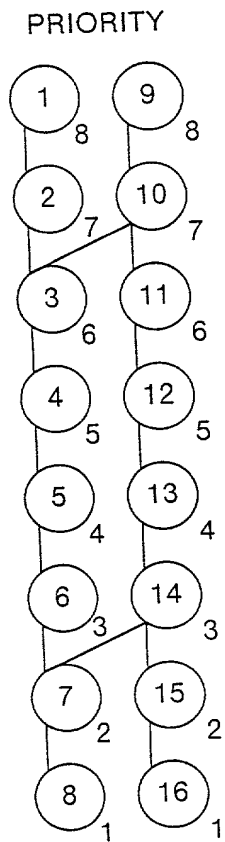


FIG.23

REVERSE PRIORITY

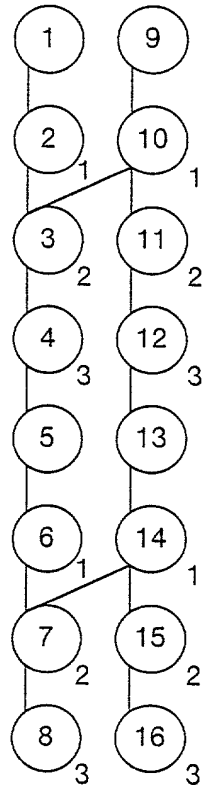


FIG.24

WEIGHT(Weight+advantage)

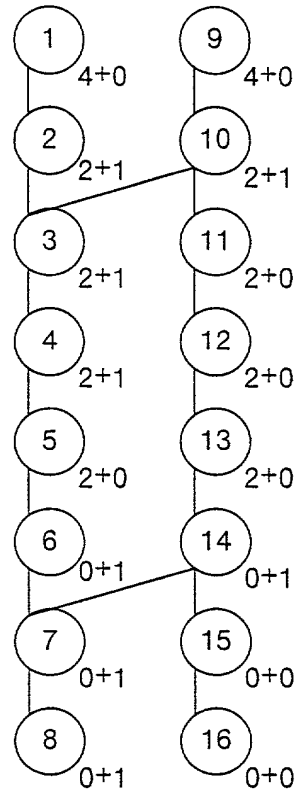


FIG.25

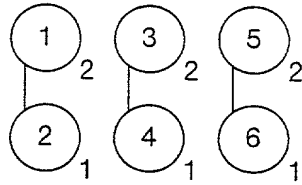


FIG.26

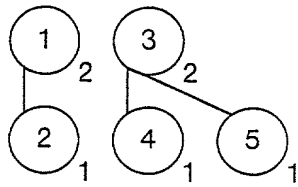


FIG.27

